

ABSTRACT OF THE DISCLOSURE

A shared bus system includes a bus, a first circuit which accesses the bus, a second circuit which shares the bus with the first circuit, 5 and accesses the bus, a counter circuit which is provided in the second circuit, and performs a counting operation each time the second circuit accesses the bus, and an arbiter circuit which arbitrates requests for a right to use the bus 10 between the first circuit and the second circuit, wherein the second circuit releases the right to use the bus in response to detection of a predetermined number of counting operations performed by the counter circuit after acquiring the right to use the 15 bus from the arbiter circuit.